



Attorney's Docket No.: 16083-138001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Briere et al.
Serial No. : 10/607,511
Filed : June 25, 2003
Title : EMI FILTER

Art Unit : Unknown
Examiner : Unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 12/8/03


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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute Form PTO-1449 (Modified)		Department of Commerce Patent and Trademark Office	Attorney's Docket No. 16083-138001	Application No. 10/607,511
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Briere et al.		
		Filing Date June 25, 2003	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2001/0045863	11/29/2001	Pelly			
	AB	5,831,842	11/03/1998	Ogasawara et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes No
	AL						
	AM						
	AN						
	AO						
	AP						

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	N.K. Poon, et al., "Techniques for Input Ripple Current Cancellation: Classification and Implementation," IEEE Transactions on Power Electronics, Vol. 15 (6), Nov. 2000, pp. 1144-1152
	AR	L. LaWhite et al., "Design of Active Ripple Filters for Power Circuits in the 1-10 MHz Range," IEEE Transactions on Power Electronics, Vol. 3 (3), July 1988, pp. 310-317
	AS	Y-C Son and S-K Sul, "A Novel Active Common-mode EMI Filter for PWM Inverter," Proceedings of the 7 th IEEE Applied Power Electronics Conference and Exposition 2002, Vol. 1, ISBN 0-7803-7404-5, pp. 545-549
	AT	S. Ogasawara et al., "An Active Circuit for Cancellation of Common-Mode Voltage Generated by a PWM Inverter," IEEE Transactions on Power Electronics, Vol. 13 (5), Sept. 1998, pp. 835-841

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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<small>DEC 11 2003</small> <small>(37 CFR §1.98(b))</small>		Applicant Briere et al.	
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Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AU	A.L. Julian et al., "Active Filtering for Common Mode Conducted EMI Reduction in Voltage Source Inverters," IEEE Trans. Industrial Applications, 1998, pp. 934-939
	AV	H.W. Ott, "Noise Reduction Techniques in Electronic Systems, John Wiley & Sons, Inc., 1988
	AW	L.E. LaWhite, "Active Filters for 1MHz Power Circuits Under Strict Ripple Limitations," MS Thesis, Massachusetts Institute of Technology, Cambridge, MA, 1987, pp. 1-99
	AX	L.E. LaWhite, M.F. Schlecht, "Active Filters for 1-MHz Power Circuits with Strict Input/Output Ripple requirements, IEEE Transactions on Power Electronics, Vol. PE-2, No. 4, 1987, pp. 282-290

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